In the Specification:

Please amend the paragraph on page 6, lines 9-10 of the specification to read as follows:

FIGURES 2A-2C are ~~FIGURE 2 is~~ an exemplary circuit as structured according to FIGURE 1;

Please amend the paragraph on page 6, lines 11-13 of the specification to read as follows:

FIGURE 3 is an exemplary graph of the output signals of the power source switchover circuitry according to FIGURES 2A-2C ~~FIGURE 2~~;

Please amend the paragraph on page 6, lines 14-16 of the specification to read as follows:

FIGURE 4 is an exemplary system block diagram having volatile elements and circuitry to provide a switchover as provided by FIGURES 1 and 2A-2C ~~2~~;

Please amend the paragraph on page 6, lines 17-19 of the specification to read as follows:

FIGURE 5 is an exemplary flow diagram for providing a switchover from the primary power source to the secondary power source according to FIGURES 1 and 2A-2C ~~2~~; and

Please amend the paragraph on page 6, line 20 to page 7, line 2 of the specification to read as follows:

FIGURE 6 is an exemplary graph of a fast decrease of the signal level of the primary power source causing the switchover to the secondary power source according to FIGURES 1 and 2A-2C ~~2~~.

Please amend the paragraph on page 13, lines 14-21 of the specification to read as follows:

FIGURES 2A-2C show ~~FIGURE 2 is~~ one embodiment for a circuit level representation of the monitor and switchover circuitry 101 according to the principles of the present invention. Dashed boxes around the various circuit elements represent each of the blocks shown in FIGURE 1, including the power source switchover circuitry 103, the switchover comparator 112, the trimming circuitry 118, the forced battery switchover circuitry 120, and the switchover logic circuitry 116.

Please amend the paragraph on page 19, line 18 to page 20, line 3 of the specification to read as follows:

Referring again to FIGURES 2A-2C ~~FIGURE 2~~, at the trip point 300, the BCOMPOUT signal 114 switches from a high to a low logic level when the primary power source is ramping up. It should be noted that the VREF signal 104 is equal to the VCCEXT 106 minus approximately 1.2 volts, which is the result of the voltage drop of the transistor 210 and the voltage drop across the resistor 215.